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(71) Applicant (for all designated States except US): TELEFONAKTIEBOLAGET LM ERICSSON (publ) [SE/SE]; S-164 83 Stockholm (SE).

(72) Inventors; and

(75) Inventors/Applicants (for US only): GEVORGIAN, Spartak [AM/SE]; Adler Salvius gata 15, S-41 1 11 Göteborg (SE). LEWIN, Thomas [SE/SE]; Landstormsvägen 3150, S-439 94 Onsala (SE). ZIRATH, Herbert [SE/SE]; Ö. Brovaktaregatan 48, S-431 36 Molndal (SE). MOTLAGH, Bahar [SE/SE]; Pennygängen 13, S-414 82 Göteborg (SE).

(74) Agents: BERGENTALL, Annika et al.; Cegumark AB, P.O. Box 53047, S-400 14 Göteborg (SE).

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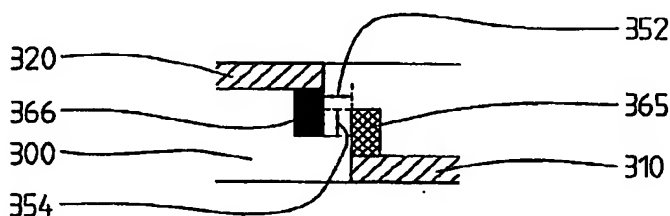
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(54) Title: CAPACITOR



(57) Abstract: A method of creating a capacitor in an integrated circuit. According to a basic version of the invention the capacitor uses intensive fringing fields to create a capacitance. This is achieved by creating a capacitor with vertical overlapping conducting electrodes between two planes of the integrated circuit, instead of plates parallel to the planes. A capacitor according to the invention can additionally comprise horizontal, i.e. parallel plates. A capacitor according the method is also disclosed.

WO 2005/062355 A1